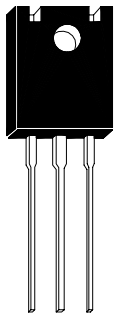
HLB121D

NPN Triple Diffused Planar Type High Voltage Transistor



TO-126ML

Description

The HLB121D is a medium power transistor designed for use in switching applications.

Features

- High breakdown voltage
- Low collector saturation voltage
- Fast switching speed

Absolute Maximum Ratings (T_A=25°C)

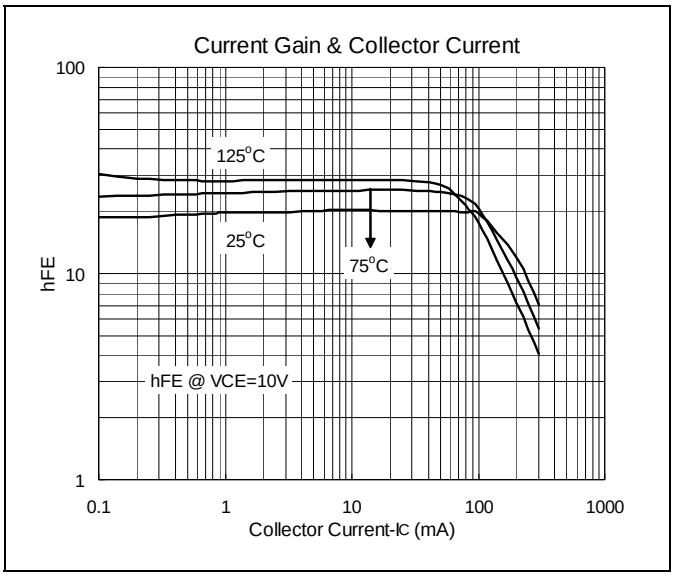
- Maximum Temperatures
  - Storage Temperature ..... -55 ~ +150 °C
  - Junction Temperature ..... +150 °C
- Maximum Power Dissipation
  - Total Power Dissipation (T_C=25°C) ..... 10 W
- Maximum Voltages and Currents
  - BV_{CBO} Collector to Base Voltage ..... 600 V
  - BV_{CEO} Collector to Emitter Voltage ..... 400 V
  - BV_{EBO} Emitter to Base Voltage ..... 6 V
  - I_C Collector Current (DC) ..... 300 mA
  - I_C Collector Current (Pulse) ..... 600 mA
  - I_B Base Current (DC) ..... 40 mA
  - I_B Base Current (Pulse) ..... 100 mA

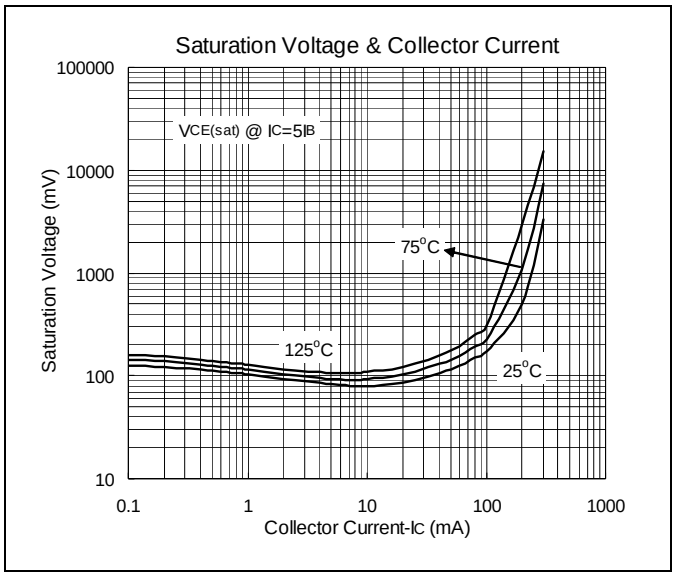
Electrical Characteristics (T_A=25°C)

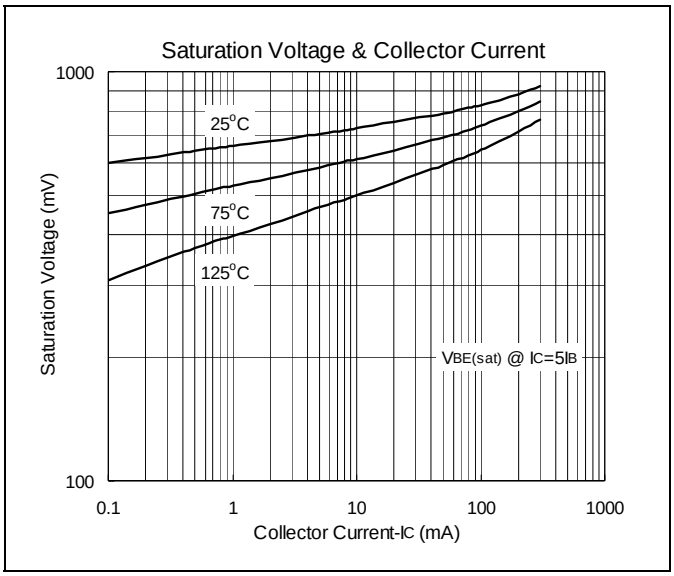
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV_{CBO}	600	-	-	V	I_C=100uA
BV_{CEO}	400	-	-	V	I_C=10mA
BV_{EBO}	6	-	-	V	I_E=10uA
I_{CBO}	-	-	10	uA	V_{CB}=550V
I_{CEO}	-	-	10	uA	V_{CB}=400V
I_{EBO}	-	-	10	uA	V_{EB}=6V
*$V_{CE(sat)1}$	-	-	400	mV	I_C=50mA, I_B=10mA
*$V_{CE(sat)2}$	-	-	750	mV	I_C=100mA, I_B=20mA
*$V_{BE(sat)}$	-	-	1	V	I_C=50mA, I_B=10mA
*h_{FE1}	8	-	-		V_{CE}=10V, I_C=10mA
*h_{FE2}	10	-	36		V_{CE}=10V, I_C=50mA

*Pulse Test: Pulse Width ≤380us, Duty Cycle≤2%

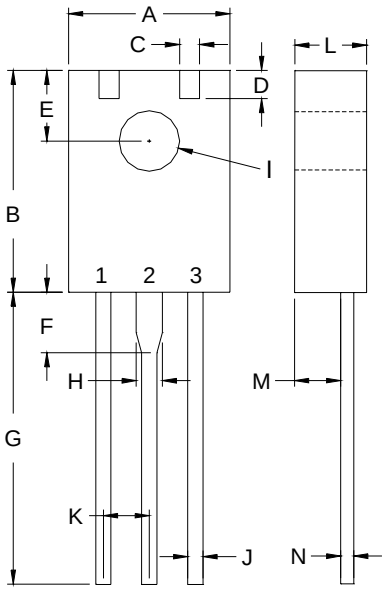
Characteristics Curve



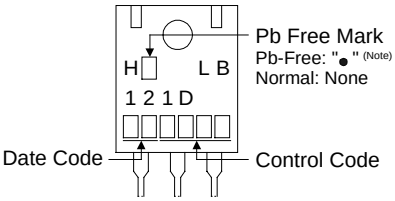




TO-126ML Dimension



Marking:



Note: Green label is used for pb-free packing

Pin Style: 1.Emitter 2.Collector 3.Base

Material:
- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

3-Lead TO-126ML
Plastic Package
HSMC Package Code: D

DIM	Min.	Max.
A	7.74	8.24
B	10.87	11.37
C	0.88	1.12
D	1.28	1.52
E	3.50	3.75
F	2.61	3.37
G	13	-
H	1.18	1.42
I	2.88	3.12
J	0.68	0.84
K	-	2.30
L	3.44	3.70
M	1.88	2.14
N	0.50	0.51

*: Typical, Unit: mm

Head Office And Factory:

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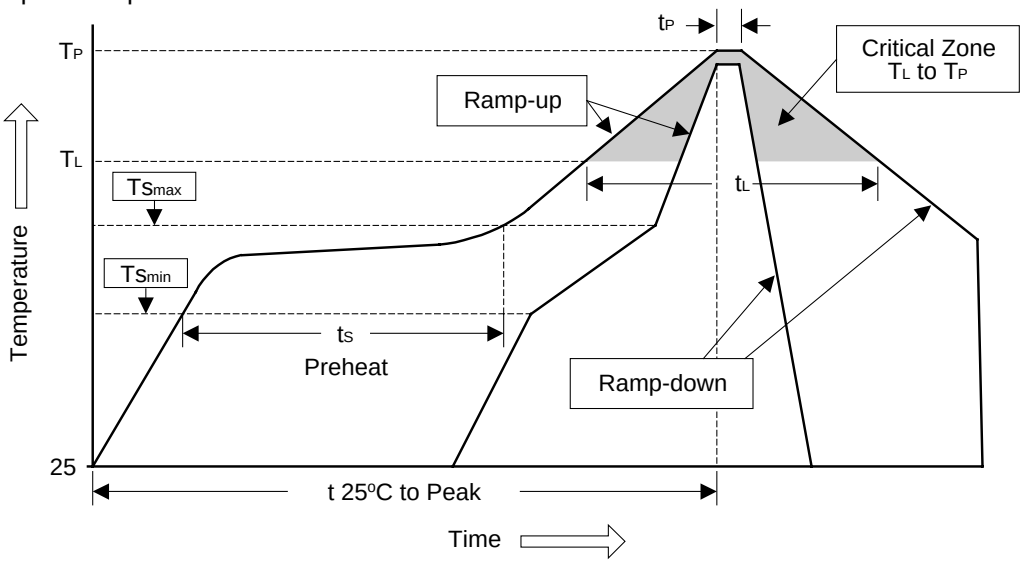
  Tel: 886-2-25212056 Fax: 886-2-25632712, 25368454
- **Factory 1:** No. 38, Kuang Fu S. Rd., Fu-Kou Hsin-Chu Industrial Park Hsin-Chu Taiwan. R.O.C
  Tel: 886-3-5983621~5 Fax: 886-3-5982931

Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%

2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat - Temperature Min (Ts_{min}) - Temperature Max (Ts_{max}) - Time (min to max) (ts)	 100°C 150°C 60~120 sec	 150°C 200°C 60~180 sec
Tsmax to T_L - Ramp-up Rate	 <3°C/sec	 <3°C/sec
Time maintained above: - Temperature (T_L) - Time (t_L)	 183°C 60~150 sec	 217°C 60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec